

# LM1575/LM2575/LM2575HV SIMPLE SWITCHER® 1A Step-Down Voltage Regulator

## General Description

The LM2575 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving a 1A load with excellent line and load regulation. These devices are available in fixed output voltages of 3.3V, 5V, 12V, 15V, and an adjustable output version.

Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensation and a fixed-frequency oscillator.

The LM2575 series offers a high-efficiency replacement for popular three-terminal linear regulators. It substantially reduces the size of the heat sink, and in many cases no heat sink is required.

A standard series of inductors optimized for use with the LM2575 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies.

Other features include a guaranteed  $\pm 4\%$  tolerance on output voltage within specified input voltages and output load conditions, and  $\pm 10\%$  on the oscillator frequency. External shutdown is included, featuring 50  $\mu\text{A}$  (typical) standby current. The output switch includes cycle-by-cycle current limiting, as well as thermal shutdown for full protection under fault conditions.

## Features

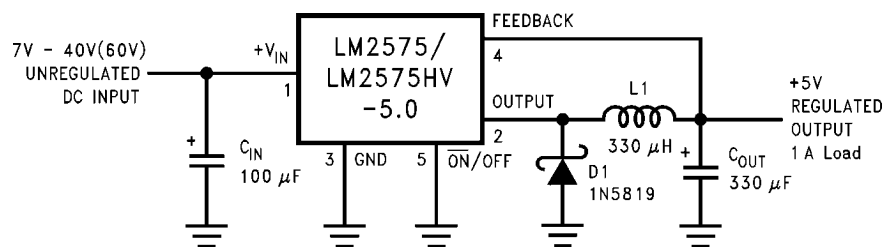
- 3.3V, 5V, 12V, 15V, and adjustable output versions
- Adjustable version output voltage range, 1.23V to 37V (57V for HV version)  $\pm 4\%$  max over line and load conditions
- Guaranteed 1A output current
- Wide input voltage range, 40V up to 60V for HV version
- Requires only 4 external components
- 52 kHz fixed frequency internal oscillator
- TTL shutdown capability, low power standby mode
- High efficiency
- Uses readily available standard inductors
- Thermal shutdown and current limit protection
- P+ Product Enhancement tested

## Applications

- Simple high-efficiency step-down (buck) regulator
- Efficient pre-regulator for linear regulators
- On-card switching regulators
- Positive to negative converter (Buck-Boost)

## Typical Application

(Fixed Output Voltage Versions)



**Note:** Pin numbers are for the TO-220 package.

1147501

## Ordering Information

Package Type	NSC Package Number	Standard Voltage Rating (40V)	High Voltage Rating (60V)	Temperature Range
5-Lead TO-220 Straight Leads	T05A	LM2575T-3.3 LM2575T-5.0 LM2575T-12 LM2575T-15 LM2575T-ADJ	LM2575HVT-3.3 LM2575HVT-5.0 LM2575HVT-12 LM2575HVT-15 LM2575HVT-ADJ	-40°C ≤ T <sub>J</sub> ≤ +125°C
5-Lead TO-220 Bent and Staggered Leads	T05D	LM2575T-3.3 Flow LB03 LM2575T-5.0 Flow LB03 LM2575T-12 Flow LB03 LM2575T-15 Flow LB03 LM2575T-ADJ Flow LB03	LM2575HVT-3.3 Flow LB03 LM2575HVT-5.0 Flow LB03 LM2575HVT-12 Flow LB03 LM2575HVT-15 Flow LB03 LM2575HVT-ADJ Flow LB03	
16-Pin Molded DIP	N16A	LM2575N-5.0 LM2575N-12 LM2575N-15 LM2575N-ADJ	LM2575HVN-5.0 LM2575HVN-12 LM2575HVN-15 LM2575HVN-ADJ	
24-Pin Surface Mount	M24B	LM2575M-5.0 LM2575M-12 LM2575M-15 LM2575M-ADJ	LM2575HVM-5.0 LM2575HVM-12 LM2575HVM-15 LM2575HVM-ADJ	
5-Lead TO-263 Surface Mount	TS5B	LM2575S-3.3 LM2575S-5.0 LM2575S-12 LM2575S-15 LM2575S-ADJ	LM2575HVS-3.3 LM2575HVS-5.0 LM2575HVS-12 LM2575HVS-15 LM2575HVS-ADJ	
16-Pin Ceramic DIP	J16A	LM1575J-3.3-QML LM1575J-5.0-QML LM1575J-12-QML LM1575J-15-QML LM1575J-ADJ-QML		-55°C ≤ T <sub>J</sub> ≤ +150°C

**Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Maximum Supply Voltage	
LM1575/LM2575	45V
LM2575HV	63V
$\overline{\text{ON}}$ /OFF Pin Input Voltage	$-0.3V \leq V \leq +V_{\text{IN}}$
Output Voltage to Ground (Steady State)	-1V
Power Dissipation	Internally Limited
Storage Temperature Range	$-65^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
Maximum Junction Temperature	150°C

Minimum ESD Rating

(C = 100 pF, R = 1.5 k $\Omega$ ) 2 kV

Lead Temperature

(Soldering, 10 sec.) 260°C

**Operating Ratings**

Temperature Range

LM1575  $-55^{\circ}\text{C} \leq T_{\text{J}} \leq +150^{\circ}\text{C}$ LM2575/LM2575HV  $-40^{\circ}\text{C} \leq T_{\text{J}} \leq +125^{\circ}\text{C}$ 

Supply Voltage

LM1575/LM2575 40V

LM2575HV 60V

**LM1575-3.3, LM2575-3.3, LM2575HV-3.3  
Electrical Characteristics**

Specifications with standard type face are for  $T_{\text{J}} = 25^{\circ}\text{C}$ , and those with **boldface type** apply over full Operating Temperature Range.

Symbol	Parameter	Conditions	Typ	LM1575-3.3	LM2575-3.3 LM2575HV-3.3	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
<b>SYSTEM PARAMETERS</b> (Note 4) Test Circuit <i>Figure 2</i>						
$V_{\text{OUT}}$	Output Voltage	$V_{\text{IN}} = 12\text{V}$ , $I_{\text{LOAD}} = 0.2\text{A}$ Circuit of <i>Figure 2</i>	3.3	3.267 3.333	3.234 3.366	V V(Min) V(Max)
$V_{\text{OUT}}$	Output Voltage LM1575/LM2575	$4.75\text{V} \leq V_{\text{IN}} \leq 40\text{V}$ , $0.2\text{A} \leq I_{\text{LOAD}} \leq 1\text{A}$ Circuit of <i>Figure 2</i>	3.3	3.200/ <b>3.168</b> 3.400/ <b>3.432</b>	3.168/ <b>3.135</b> 3.432/ <b>3.465</b>	V V(Min) V(Max)
$V_{\text{OUT}}$	Output Voltage LM2575HV	$4.75\text{V} \leq V_{\text{IN}} \leq 60\text{V}$ , $0.2\text{A} \leq I_{\text{LOAD}} \leq 1\text{A}$ Circuit of <i>Figure 2</i>	3.3	3.200/ <b>3.168</b> 3.416/ <b>3.450</b>	3.168/ <b>3.135</b> 3.450/ <b>3.482</b>	V V(Min) V(Max)
$\eta$	Efficiency	$V_{\text{IN}} = 12\text{V}$ , $I_{\text{LOAD}} = 1\text{A}$	75			%

**LM1575-5.0, LM2575-5.0, LM2575HV-5.0  
Electrical Characteristics**

Specifications with standard type face are for  $T_{\text{J}} = 25^{\circ}\text{C}$ , and those with **boldface type** apply over full Operating Temperature Range.

Symbol	Parameter	Conditions	Typ	LM1575-5.0	LM2575-5.0 LM2575HV-5.0	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
<b>SYSTEM PARAMETERS</b> (Note 4) Test Circuit <i>Figure 2</i>						
$V_{\text{OUT}}$	Output Voltage	$V_{\text{IN}} = 12\text{V}$ , $I_{\text{LOAD}} = 0.2\text{A}$ Circuit of <i>Figure 2</i>	5.0	4.950 5.050	4.900 5.100	V V(Min) V(Max)
$V_{\text{OUT}}$	Output Voltage LM1575/LM2575	$0.2\text{A} \leq I_{\text{LOAD}} \leq 1\text{A}$ , $8\text{V} \leq V_{\text{IN}} \leq 40\text{V}$ Circuit of <i>Figure 2</i>	5.0	4.850/ <b>4.800</b> 5.150/ <b>5.200</b>	4.800/ <b>4.750</b> 5.200/ <b>5.250</b>	V V(Min) V(Max)

Symbol	Parameter	Conditions	Typ	LM1575-5.0	LM2575-5.0 LM2575HV-5.0	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
$V_{OUT}$	Output Voltage LM2575HV	$0.2A \leq I_{LOAD} \leq 1A$ , $8V \leq V_{IN} \leq 60V$ Circuit of <i>Figure 2</i>	5.0	4.850/ <b>4.800</b> 5.175/ <b>5.225</b>	4.800/ <b>4.750</b> 5.225/ <b>5.275</b>	V V(Min) V(Max)
$\eta$	Efficiency	$V_{IN} = 12V$ , $I_{LOAD} = 1A$	77			%

### LM1575-12, LM2575-12, LM2575HV-12 Electrical Characteristics

Specifications with standard type face are for  $T_J = 25^\circ C$ , and those with **boldface type** apply over full Operating Temperature Range .

Symbol	Parameter	Conditions	Typ	LM1575-12	LM2575-12 LM2575HV-12	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	

#### SYSTEM PARAMETERS (Note 4) Test Circuit *Figure 2*

$V_{OUT}$	Output Voltage	$V_{IN} = 25V$ , $I_{LOAD} = 0.2A$ Circuit of <i>Figure 2</i>	12	11.88 12.12	11.76 12.24	V V(Min) V(Max)
$V_{OUT}$	Output Voltage LM1575/LM2575	$0.2A \leq I_{LOAD} \leq 1A$ , $15V \leq V_{IN} \leq 40V$ Circuit of <i>Figure 2</i>	12	11.64/ <b>11.52</b> 12.36/ <b>12.48</b>	11.52/ <b>11.40</b> 12.48/ <b>12.60</b>	V V(Min) V(Max)
$V_{OUT}$	Output Voltage LM2575HV	$0.2A \leq I_{LOAD} \leq 1A$ , $15V \leq V_{IN} \leq 60V$ Circuit of <i>Figure 2</i>	12	11.64/ <b>11.52</b> 12.42/ <b>12.54</b>	11.52/ <b>11.40</b> 12.54/ <b>12.66</b>	V V(Min) V(Max)
$\eta$	Efficiency	$V_{IN} = 15V$ , $I_{LOAD} = 1A$	88			%

### LM1575-15, LM2575-15, LM2575HV-15 Electrical Characteristics

Specifications with standard type face are for  $T_J = 25^\circ C$ , and those with **boldface type** apply over full Operating Temperature Range .

Symbol	Parameter	Conditions	Typ	LM1575-15	LM2575-15 LM2575HV-15	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	

#### SYSTEM PARAMETERS (Note 4) Test Circuit *Figure 2*

$V_{OUT}$	Output Voltage	$V_{IN} = 30V$ , $I_{LOAD} = 0.2A$ Circuit of <i>Figure 2</i>	15	14.85 15.15	14.70 15.30	V V(Min) V(Max)
$V_{OUT}$	Output Voltage LM1575/LM2575	$0.2A \leq I_{LOAD} \leq 1A$ , $18V \leq V_{IN} \leq 40V$ Circuit of <i>Figure 2</i>	15	14.55/ <b>14.40</b> 15.45/ <b>15.60</b>	14.40/ <b>14.25</b> 15.60/ <b>15.75</b>	V V(Min) V(Max)
$V_{OUT}$	Output Voltage LM2575HV	$0.2A \leq I_{LOAD} \leq 1A$ , $18V \leq V_{IN} \leq 60V$ Circuit of <i>Figure 2</i>	15	14.55/ <b>14.40</b> 15.525/ <b>15.675</b>	14.40/ <b>14.25</b> 15.68/ <b>15.83</b>	V V(Min) V(Max)
$\eta$	Efficiency	$V_{IN} = 18V$ , $I_{LOAD} = 1A$	88			%

## LM1575-ADJ, LM2575-ADJ, LM2575HV-ADJ Electrical Characteristics

Specifications with standard type face are for  $T_J = 25^\circ\text{C}$ , and those with **boldface type** apply over **full Operating Temperature Range**.

Symbol	Parameter	Conditions	Typ	LM1575-ADJ	LM2575-ADJ LM2575HV-ADJ	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
<b>SYSTEM PARAMETERS</b> (Note 4) Test Circuit <i>Figure 2</i>						
$V_{OUT}$	Feedback Voltage	$V_{IN} = 12\text{V}$ , $I_{LOAD} = 0.2\text{A}$ $V_{OUT} = 5\text{V}$ Circuit of <i>Figure 2</i>	1.230	1.217 1.243	1.217 1.243	V V(Min) V(Max)
$V_{OUT}$	Feedback Voltage LM1575/LM2575	$0.2\text{A} \leq I_{LOAD} \leq 1\text{A}$ , $8\text{V} \leq V_{IN} \leq 40\text{V}$ $V_{OUT} = 5\text{V}$ , Circuit of <i>Figure 2</i>	1.230	1.205/ <b>1.193</b> 1.255/ <b>1.267</b>	1.193/ <b>1.180</b> 1.267/ <b>1.280</b>	V V(Min) V(Max)
$V_{OUT}$	Feedback Voltage LM2575HV	$0.2\text{A} \leq I_{LOAD} \leq 1\text{A}$ , $8\text{V} \leq V_{IN} \leq 60\text{V}$ $V_{OUT} = 5\text{V}$ , Circuit of <i>Figure 2</i>	1.230	1.205/ <b>1.193</b> 1.261/ <b>1.273</b>	1.193/ <b>1.180</b> 1.273/ <b>1.286</b>	V V(Min) V(Max)
$\eta$	Efficiency	$V_{IN} = 12\text{V}$ , $I_{LOAD} = 1\text{A}$ , $V_{OUT} = 5\text{V}$	77			%

## All Output Voltage Versions Electrical Characteristics

Specifications with standard type face are for  $T_J = 25^\circ\text{C}$ , and those with **boldface type** apply over **full Operating Temperature Range**. Unless otherwise specified,  $V_{IN} = 12\text{V}$  for the 3.3V, 5V, and Adjustable version,  $V_{IN} = 25\text{V}$  for the 12V version, and  $V_{IN} = 30\text{V}$  for the 15V version.  $I_{LOAD} = 200\text{mA}$ .

Symbol	Parameter	Conditions	Typ	LM1575-XX	LM2575-XX LM2575HV-XX	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
<b>DEVICE PARAMETERS</b>						
$I_b$	Feedback Bias Current	$V_{OUT} = 5\text{V}$ (Adjustable Version Only)	50	100/ <b>500</b>	100/ <b>500</b>	nA
$f_o$	Oscillator Frequency	(Note 13)	52	47/ <b>43</b> 58/ <b>62</b>	47/ <b>42</b> 58/ <b>63</b>	kHz kHz(Min) kHz(Max)
$V_{SAT}$	Saturation Voltage	$I_{OUT} = 1\text{A}$ (Note 5)	0.9	1.2/ <b>1.4</b>	1.2/ <b>1.4</b>	V V(Max)
DC	Max Duty Cycle (ON)	(Note 6)	98	93	93	% %(Min)
$I_{CL}$	Current Limit	Peak Current (Notes 5, 13)	2.2	1.7/ <b>1.3</b> 3.0/ <b>3.2</b>	1.7/ <b>1.3</b> 3.0/ <b>3.2</b>	A A(Min) A(Max)
$I_L$	Output Leakage Current	(Notes 7, 8) Output = 0V Output = -1V Output = -1V	7.5	2 30	2 30	mA(Max) mA mA(Max)
$I_Q$	Quiescent Current	(Note 7)	5	10/ <b>12</b>	10	mA mA(Max)
$I_{STBY}$	Standby Quiescent Current	$\overline{ON}$ /OFF Pin = 5V (OFF)	50	200/ <b>500</b>	200	$\mu\text{A}$ $\mu\text{A}$ (Max)

Symbol	Parameter	Conditions	Typ	LM1575-XX	LM2575-XX LM2575HV-XX	Units (Limits)
				Limit (Note 2)	Limit (Note 3)	
$\theta_{JA}$	Thermal Resistance	T Package, Junction to Ambient (Note 9)	65			°C/W
$\theta_{JA}$		T Package, Junction to Ambient (Note 10)	45			
$\theta_{JC}$		T Package, Junction to Case	2			
$\theta_{JA}$		N Package, Junction to Ambient (Note 11)	85			
$\theta_{JA}$		M Package, Junction to Ambient (Note 11)	100			
$\theta_{JA}$		S Package, Junction to Ambient (Note 12)	37			

**ON /OFF CONTROL Test Circuit** *Figure 2*

$V_{IH}$	ON /OFF Pin Logic Input Level	$V_{OUT} = 0V$	1.4	<b>2.2/2.4</b>	<b>2.2/2.4</b>	V(Min)
$V_{IL}$		$V_{OUT} = \text{Nominal Output Voltage}$	1.2	<b>1.0/0.8</b>	<b>1.0/0.8</b>	V(Max)
$I_{IH}$	ON /OFF Pin Input Current	ON /OFF Pin = 5V (OFF)	12	30	30	$\mu A$ $\mu A(\text{Max})$
$I_{IL}$		ON /OFF Pin = 0V (ON)	0	10	10	$\mu A$ $\mu A(\text{Max})$

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.

**Note 2:** All limits guaranteed at room temperature (standard type face) and at **temperature extremes (bold type face)**. All limits are used to calculate Average Outgoing Quality Level, and all are 100% production tested.

**Note 3:** All limits guaranteed at room temperature (standard type face) and at **temperature extremes (bold type face)**. All room temperature limits are 100% production tested. All limits at **temperature extremes** are guaranteed via correlation using standard Statistical Quality Control (SQC) methods.

**Note 4:** External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance. When the LM1575/LM2575 is used as shown in the *Figure 2* test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.

**Note 5:** Output (pin 2) sourcing current. No diode, inductor or capacitor connected to output pin.

**Note 6:** Feedback (pin 4) removed from output and connected to 0V.

**Note 7:** Feedback (pin 4) removed from output and connected to +12V for the Adjustable, 3.3V, and 5V versions, and +25V for the 12V and 15V versions, to force the output transistor OFF.

**Note 8:**  $V_{IN} = 40V$  (60V for the high voltage version).

**Note 9:** Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with ½ inch leads in a socket, or on a PC board with minimum copper area.

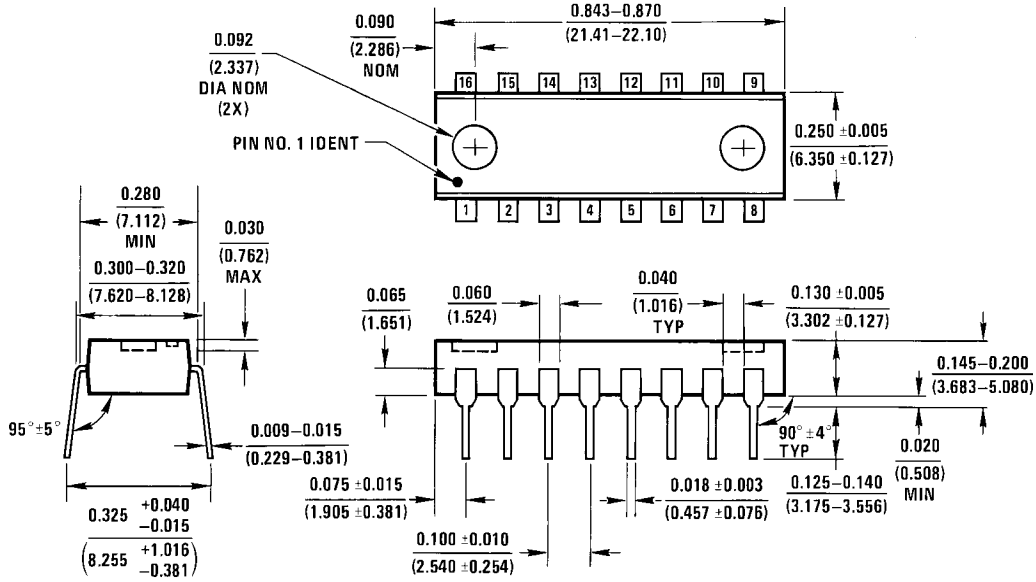
**Note 10:** Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with ½ inch leads soldered to a PC board containing approximately 4 square inches of copper area surrounding the leads.

**Note 11:** Junction to ambient thermal resistance with approximately 1 square inch of pc board copper surrounding the leads. Additional copper area will lower thermal resistance further. See thermal model in Switchers made Simple software.

**Note 12:** If the TO-263 package is used, the thermal resistance can be reduced by increasing the PC board copper area thermally connected to the package: Using 0.5 square inches of copper area,  $\theta_{JA}$  is 50°C/W; with 1 square inch of copper area,  $\theta_{JA}$  is 37°C/W; and with 1.6 or more square inches of copper area,  $\theta_{JA}$  is 32°C/W.

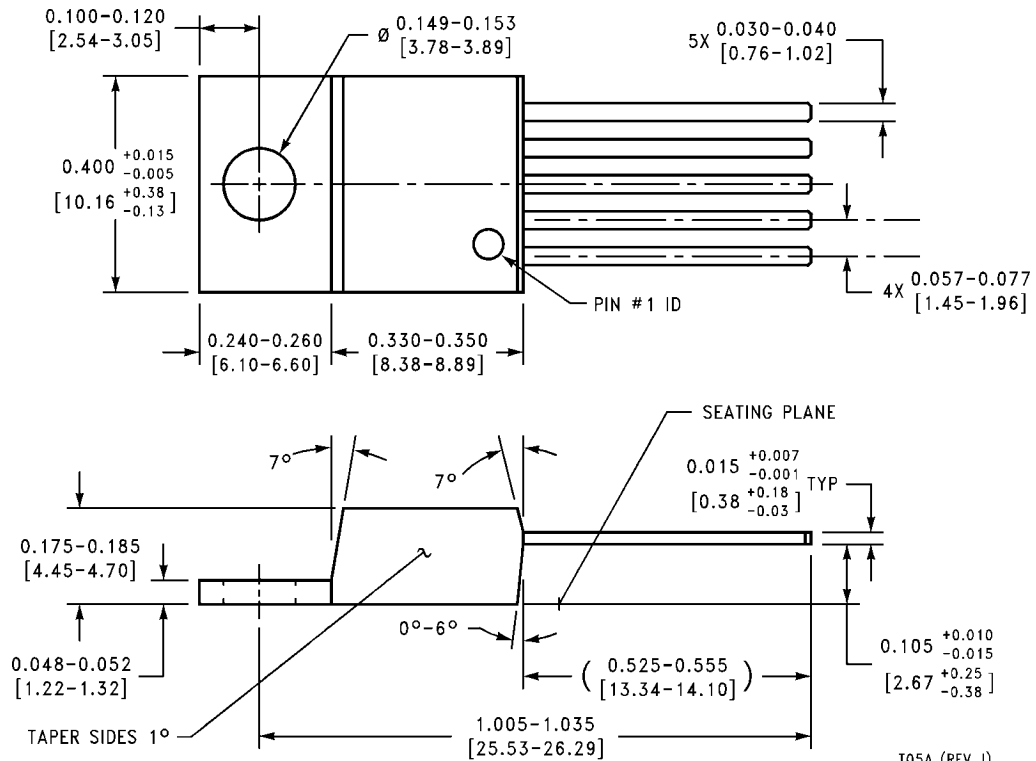
**Note 13:** The oscillator frequency reduces to approximately 18 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately 40% from the nominal output voltage. This self protection feature lowers the average power dissipation of the IC by lowering the minimum duty cycle from 5% down to approximately 2%.

**Note 14:** Refer to RETS LM1575J for current revision of military RETS/SMD.



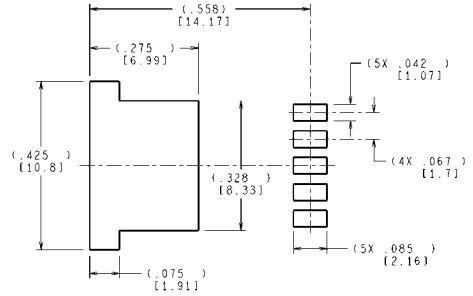
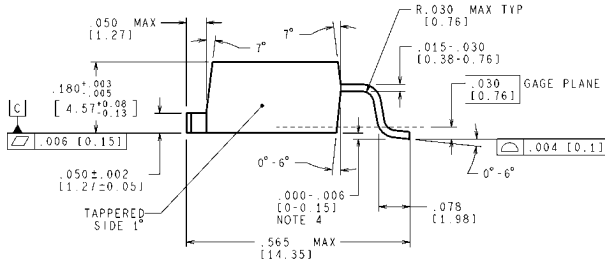
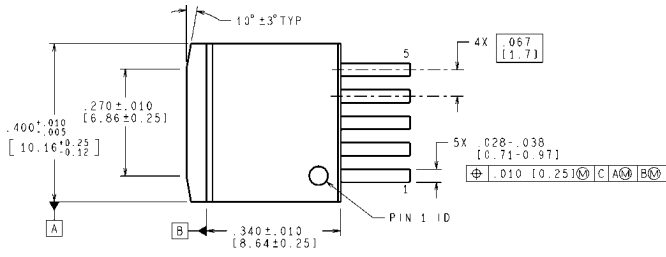
N16A (REV E)

**16-Lead Molded DIP (N)**  
**Order Number LM2575N-5.0, LM2575HVN-5.0, LM2575N-12, LM2575HVN-12,**  
**LM2575N-15, LM2575HVN-15, LM2575N-ADJ or LM2575HVN-ADJ**  
**NS Package Number N16A**

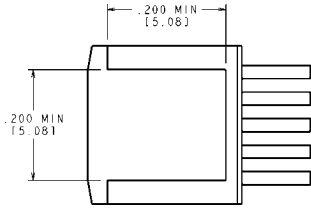


T05A (REV J)

**5-Lead TO-220 (T)**  
**Order Number LM2575T-3.3, LM2575HVT-3.3, LM2575T-5.0, LM2575HVT-5.0, LM2575T-12,**  
**LM2575HVT-12, LM2575T-15, LM2575HVT-15, LM2575T-ADJ or LM2575HVT-ADJ**  
**NS Package Number T05A**



LAND PATTERN RECOMMENDATION



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TSSB (Rev D)

**TO-263, Molded, 5-Lead Surface Mount**  
**Order Number LM2575S-3.3, LM2575HVS-3.3, LM2575S-5.0, LM2575HVS-5.0, LM2575S-12,**  
**LM2575HVS-12, LM2575S-15, LM2575HVS-15, LM2575S-ADJ or LM2575HVS-ADJ**  
**NS Package Number TS5B**